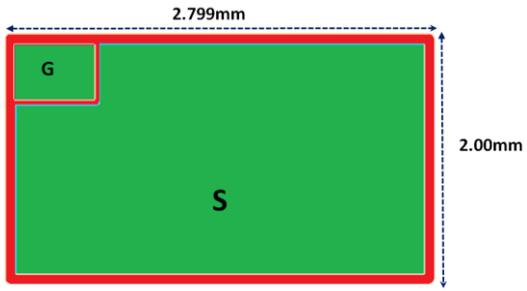


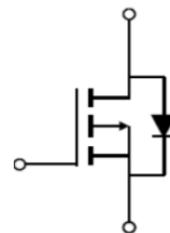
Mechanical Date

Die Size	2799×2000	μm ²	
Gate Pad Size	280×440		
Source Pad Size	No Passivation		
Scribe Line Size	60	μm	
Wafer Diameter	200	mm	
Wafer Thickness	175-200	μm	
Passivation Frontside	No Passivation	---	
Source Metallization	AL	4.0	
Drain Metallization	Ti- Ni - Ag	1.3	
Reject Ink Dot Size	0.51	mm	
Recommended Storage Environment	Store in original container, in dessicated nitrogen, with no contamination		

Electrical Characteristics(T_J=25°C)

Parameter	Description	Min.	Typ.	Max.	Unit	Test Condition
V _{(BR)DSS}	Drain-Source Breakdown Voltage	-100			V	V _{GS} =0V, I _D =-250μA
R _{DS(on)1}	Static Drain-Source On-Resistance			50	mΩ	V _{GS} =-10V, I _D =-16A
R _{DS(on)2}	Static Drain-Source On-Resistance			55	mΩ	V _{GS} =-4.5V, I _D =-10A
V _{GS(th)}	Gate Threshold Voltage	-1.0		-3.0	V	V _{DS} =V _{GS} , I _D =-250μA
I _{DSS}	Drain-to-Source Leakage Current			1	μA	V _{DS} =-100V, V _{GS} =0V, T _J =25°C
I _{GSS}	Gate-Body Leakage Current			±100	nA	V _{GS} =±20V
V _{SD}	Body Diode Voltage			1.5	V	V _{GS} =0V, I _{SD} =-35A
T _J , T _{stg}	Operating and Storage Temperature Range	-55~+150			°C	

- -100V、-35A*, P-channel
- R_{DS(on)}=50mΩ(MAX)
- Ultra low Q_{gd}
- Fast switching



* Electrical characteristics are reported for the reference packaged part (TO-251/252/220) and can not be guaranteed in die

sales form.

Variations in customer packaging materials, dimensions and processes may affect parametric performance.